

Notice of References Cited	Application/Control No. 10/507,408	Applicant(s)/Patent Under Reexamination FRENZEL ET AL.	
	Examiner Sam Dillon	Art Unit 2185	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,809,533	09-1998	Tran et al.	711/141
*	B	US-5,412,788	05-1995	Collins et al.	711/157
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Opsommer, J.; D'Hollander, E.H., "A VLSI processor-switch for a dual IEEE-796 bus with shared and dual-port memories," CompEuro '89., 'VLSI and Computer Peripherals. VLSI and Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks', Proceedings., vol., no.pp.4/146-4/149, 8-12 May 1989
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.